Product Specification

Part Name: OEL Display Module
Customer Part ID:
Allvision Part ID: ZJY-6428TSWOG01
Ver: A

Customer:
Approved by

From: ZhongJY technology Inc.
Approved by

Notes:
1. Please contact ZJY technology Inc. before assi   gning your product based on this module specification
2. The information contained herein is presented merely to indicate the characteristics and performance of our products.   No responsibility is assumed by Allvision technology Inc. for any intellectual property claims or other problems that may result from application based on the module described herein.

Revised History

Part Number	Revision	Revision Content	Revised on
ZJY-6428TSWOG01 A	New	20171127	

Contents

1. Basic Specifications

1.1 Display Specifications

1) Display Mode: Passive Matrix
2) Display Color: Monochrome (White)
3) Drive Duty: 1/128 Duty

1.2 Mechanical Specifications

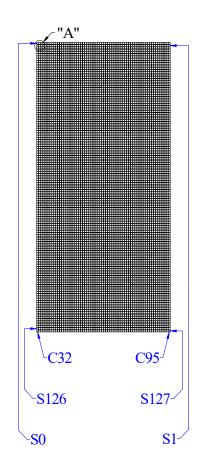1) Outline Drawing: According to the annexed outline drawing
2) Number of Pixels: 64 × 128
3) Panel Size: 17.1 × 35.8 × 1.43 (mm)
4) Active Area: 14.7 × 29.42 (mm)
5) Pixel Pitch: 0.17 × 0.17 (mm)
6) Pixel Size: 0.15 × 0.15 (mm)
7) Weight: TBD

1.3 Active Area / Memory Mapping & Pixel Construction

Detail "A"
Scale (10:1)

1.4　　Mechanical Drawing

Notes:
1. Color: White
2. Driver IC: SH1107
3. FPC Number: QT1107P11
4. Interface:
   4-wire SPI, I2C
5. General Tolerance: ±0.30

Detail "A"
Scale (10:1)

64(c)x128(s)
(1.3")

ZJY Technology Inc.

Title	QG-6428TSWOG01 Folding Type OEL Display Module Pixel Number:64 x 128, Monochrome, COG Package
Drawn	HONG
Date	20171017

Drawing Number	QG-2864TSWOG01
Material	Soda Lime / Polyimide
Scale	1:1	Sheet	1 of 1	Size	A3
Rev.	A

Pin	Symbol
1	NC
2	VPP
3	VCOMH
4	VDD
5	IM1
6	IREF
7	CS
8	RES
9	DC
10	D0
11	D1
12	VSS
13	NC

Item	Date	Remark
A	20171017	Original Drawing

Unless Otherwise Specified
Unit	mm
Dimension	±0.3
Angle	±1

Customer Approval
Signature

1.5   Pin Definition

Pin Number	Symbol	I/O	Function
Power Supply			
2	VPP	P	Power Supply for OEL Panell This is the most positive voltage supply pin of the chip. It must be supplied externally.
4	VDD	P	Power Supply for Logic This is a voltage supply pin. It must be connected to external source.
12	VSS	P	Ground of OEL System This is a ground pin. It also acts as a reference for the logic pins, the OEL driving voltages, and the analog circuits. It must be connected to external ground.
Driver			
6	IREF	I	**Current Reference for Brightness Adjustment** This pin is segment current reference pin. A resistor should be connected between this pin and V_{SS}. Set the current at 12.5μA maximum.
3	VCOMH	O	**Voltage Output High Level for COM Signal** This pin is the input pin for the voltage output high level for COM signals. A capacitor should be connected between this pin and V_{SS}.
Interface			
5	IM1	I	Communicating Protocol Select These pins are MCU interface selection input. See the following table: |	IM1 | | 4-wire SPI | 0 | | I2C | 1 |
8	RES#	I	**Power Reset for Controller and Driver** This pin is reset signal input. When the pin is low, initialization of the chip is executed. Keep this pin pull high during normal operation.
7	CS#	I	**Chip Select** This pin is the chip select input. The chip is enabled for MCU communication only when CS# is pulled low.
9	A0	I	Data/Command Control This pin is Data/Command control pin. When the pin is pulled high, the input at D7~D0 will be interpreted as display data. When the pin is pulled low, the input at D7~D0 will be transferred to the command register. When the pin is pulled high and serial interface mode is selected, the data at SI will be interpreted as data. When it is pulled low, the data at SI will be transferred to the command register. In I2C mode, this pin acts as SA0 for slave address selection. For detail relationship to MCU interface signals, please refer to the Timing Characteristics Diagrams.
10,11	D0,D1	I/O	Serial Data Input/Output and clock When serial mode is selected, D1 will be the serial data input SI and D0 will be the serial clock input SCL. When I2C mode is selected, D1 be the serial data input SDA and D0 is the serial clock input, SCL.
Reserve			
1,13	NC	-	Reserved Pin The N.C. pins between function pins are reserved for compatible and flexible design.