Item	Description	Specifications
		4. Single-channel (64 bits wide) DDR operation supported 5. Up to 2 GB system memory 6. Supports DDR channel operation at 266 MHz, 333 MHz and 400 MHz with a Peak BW of 2.1 GB/s, 2.7 GB/s, and 3.2GB/s respectively per channel 7. AGP 3.0 with 4X / 8X AGP data transfers and 4X / 8X fast writes, respectively 8. Integrated Graphics 9. AGP signals multiplexed with two DVO ports (ADD card supported) 10. Analog Display Support — 350 MHz Integrated 24-bit RAMDAC — Up to 2048x1536 @ 75 Hz refresh 11. 932 solder balls with variable ball pitch
3	South Bridge	Intel ICH5: 1. 6 available PCI REQ/GNT pairs 2. Integrated LAN Controller 3. Integrated Serial ATA Host Controllers 4. Integrated 2 IDE Controller 5. USB 2.0: — New: Includes 4 UHCI Host Controllers, increasing the number of external ports to eight 6. Support for 3 AC '97 2.3 codecs. 7. New: ACPI 2.0 compliant 8. Real-Time Clock 9. Enhanced DMA Controller 10. SMBus — New: Provides independent manageability bus through SMLink interface.
4	System Memory	PC2700/ PC2100/PC1600 DDR SDRAM compliant. 2 DDR-DIMM supporting up to 2GB.
5	BIOS ROM	Phoenix BIOS code base. 512KB BB Flash ROM without socket.
6	LCD	TFT panel only, 17" : Support SXGA (1280X1024).
7	VGA Subsystem	*UMA: TV-Out: • VGA to TV conversion supporting up to 1024x768 • MacrovisionTM 7.1.L1 copy protection support • Two variable-voltage digital input ports. • Simultaneous LVDS and TV output. • TrueScaleTM rendering engine supports under-scan in all TV output resolutions • Enhanced text sharpness and adaptive flicker removal with up to 7 lines of filtering

Item	Description	Specifications
		• Support for all NTSC and PAL TV formats • Outputs CVBS, S-Videc, RGB and YprPb • Support for SCART connector • TV / Monitor connection detect • Output video switch for easy wiring to connectors LVDS-Out: • Single / Dual LVDS transmitter • Dual LVDS supports pixel rate up to 330Mpixels/sec. when both 12-bit input ports are ganged together. • Panel fitting scaler – up scale to 1600x1200 • LVDS low jitter PLL accepts spread spectrum input • LVDS 18-bit and 24-bit output • 2D dither engine for 18-bit panels • Panel protection and power down sequencing • Programmable power management • Hot Plug detection • Support for second CRT DAC bypass mode • Four 10-bit videc DAC outputs • Fully programmable through serial port • Complete Windows and DOS driver support • Variable voltage interface to graphics device • Offered in a 128-pin LQFP package *nVIVIA NV34, NV36: 1. Microsoft® DirectX® 9.0 and OpenGL 2.0 graphics APIs hardware support with NVIDIA extensions 2. AGP3.0 8X (with Fast Writes and Side Band signaling support) for enhanced host Includes AGP3.0 and AGP2.0 4X, 2X and 1X 3. Enhanced NVIDIA nView™ multi-display technology supporting any combination of notebook LCD, desktop VGA monitor, DVI display, or TV set. Includes new user controls, application controls and interface support capabilities 4. Dual CRTC/simultaneous dual display (same or different surfaces) 5. Integrated dual LVDS transmitter supporting LCD panels up to 2048 × 1536 at 60 Hz 6. Dual, integrated 400 MHz RAMDACs for display resolutions up to and including 2048 × 1536 at 85 Hz refresh 7. Built-in HDTV component videc out support: 1080i and 720p resolutions 8. Integrated TMDS transmitter for Digital Visual Interface

Item	Description	Specifications
		(DVI) support with scaling and filtering for flat panels up to 1600 × 1200 at 60 Hz 9. DVD- and HDTV-ready MPEG-2 decoding up to 1920 × 1080i resolutions 10. Dual 12-bit or single 24-bit configurable DVO ports for up to 333 megapixels/sec transfer rates. The DVO interface provides a fully independent 12-bit DVO port, while the other 12 bits of the configurable DVO port are signals multiplexed with the VIP 1.1 port.
8	PCMCIA	ENE CB1410: 1. Compliant with - PCI Local Bus Specification, Revision 2.2 - PCI Bus Power Management Interface Specification, Revision 1.1 - PCI Mobile Design Guide, Version 1.1 - Advanced Configuration and Power Interface Specification, Revision 1.0 - PC 99 System Design Guide - PC Card Standard 8.0 2. Interrupt configuration - Supports parallel PCI interrupts - Supports parallel IRQ and parallel PCI interrupts - Supports serialized IRQ and parallel PCI interrupts - Supports serialized IRQ and PCI interrupts 3. Power Management Control Logic - Supports CLKRUN# protocol - Supports SUSPEND# - Supports PCI PME# from D3, D2, D1 - Supports PCI PME# from D3Cold - Supports D3STATE# (CB1410 only) 4. CB1410 supports parallel 4 wire power switch interface.
9	1394	TI TSB43AB22: 1. Two IEEE Std 1394a-2000 fully compliant cable port working up to 400M bits/s. 2. Fully compliant with IEEE 1394 standard. 3. Power-down features to conserve energy in battery-powered. 4. Ultra-low-power sleep mode 5. Serial ROM interface supports 2-wire serial EEPROM for ID usage.
10	Super I/O	NS PC87391: 1. One IEEE 1284 compliant parallel port support. 2. LPC v1.01 compliant.

Item	Description	Specifications
		3. X-Bus Extension for read and write operations 4. PC99 and ACPI compliant. 5. Serial IRQ support. 6. IrDA 1.1 compliant.
11	KBC	NS87591: 1. Internal keyboard interface. 2. X-Bus interface to system. 3. 16-bit embedded RISC processor core. 4. 2048 bytes of on-chip ROM/1024 bytes of on-chip RAM. 5. 8042 KBC standard Interface (60h, 64h). 6. IRQ1 and IRQ12 support. 7. Low power consumption in idle mode (20 mA). 8. PS/2 interface support for touch-pad. 9. Intel SMBus and Philips I2C® compatible. 10. Support up to 8 channels, 8-bit resolution ADC. 11. Support up to 4 channels, 8-bit resolution DAC. 12. Supports APM v1.2.
12	Audio Codec	Realtek ALC202 AC97 Codec 1. Internal stereo speaker, internal microphone, & Line-In, Line-Out, & Mic-In jacks. 2. Single chip audio CODEC with high S/N ratio (>90 dB). 3. Compliant with AC' 97 2.2 & WHQL specifications. 4. Meets performance requirements for audio on PC2001 systems. 5. Meets Microsoft PC99 & WLP 2.0 audio requirements. 6. 18-bit Stereo full-duplex CODEC with independent and variable sampling rate. 7. 18-bit ADC and 20-bit DAC resolution. 8. +30dB boost preamplifier for MIC input 9. Stereo output with 6-bit volume control, and mono output with 5-bit volume control. 10. Headphone output with 50mW/8ohm driving capability.
13	LAN	Broadcom 5788M embedded LAN controller & LAN PHYceive: 1. Standard MII interface between controller & PHYceiver 2. 10/100/1000 MHz full and half duplex operation with auto-negotiation. 3. Independent 2K byte FIFOs for receive and transmit 4. Flexible dynamically loadable EEPROM algorithm 5. Magic packet and wake-on-address filtering 6. Software controllable power down
14	Memory card reader (option)	6-In-1 daughter card (factory optional): 1. Support Secure Digital , Smart Media, Multi-Media Card, and

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